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REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2815

PATENT
3430-0131P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: HWANG, Kwang-Jo

CONF.: 5562

SERIAL NO.: 09/648,111

GROUP: 2815

FILED: August 25, 2000

EXAMINER: Brock II, P.

FOR: METHOD OF PATTERNING A METAL LAYER IN A SEMICONDUCTOR DEVICE

LARGE ENTITY TRANSMITTAL FORM
FOR REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

BOX AFAssistant Commissioner for Patents
Washington, DC 20231

December 2, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMEN T					HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	30	-	30	=	0			\$ 18	\$0.00
INDEPENDENT	4	-	3	=	1			\$ 84	\$84.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM								\$280	\$0.00
								TOTAL	\$84.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$84.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.


If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

Joseph A. Kolasch, #22,463


JAK/PLS/asc
3430-0131P

P.O. Box 747
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Attachment(s)

(Rev. 09/30/02)



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PATENT
3430-0131P
TECHNOLOGY CENTER 2800
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): HWANG, Kwang-Jo ✓ CONF.: 5562
SERIAL NO.: 09/648,111 ✓ GROUP: 2815
FILED: August 25, 2000 ✓ EXAMINER: Brock II, P.
FOR: METHOD OF PATTERNING A METAL LAYER IN A
SEMICONDUCTOR DEVICE

#1410
And
J. Macmillan
12/9/02

REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

Assistant Commissioner for Patents
Washington, D.C. 20231

December 2, 2002

Dear Sir:

Entered
J. Macmillan
1/15/03

In response to the final Office Action dated September 9, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please amend the claims as follows:

1. (Three Times Amended) A method of manufacturing a liquid crystal display device, comprising:

forming a switching element on a substrate;

forming a passivation layer over the substrate;

depositing a metal layer on the passivation layer;

12/03/2002 MGBREM1 00000042 0964811P

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